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Bib Data Sheet

CONFIRMATION NO. 9733

SERIAL NUMBER	FILING DATE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.
10/774,406	02/10/2004 RULE	438	2813	501.37465CC8

APPLICANTS

Yoshikazu Tanabe, Iruma-shi, JAPAN;

Satoshi Sakai, Tokyo, JAPAN;
Nobuyoshi Natsuaki, Tokyo, JAPAN;

** CONTINUING DATA ********** Yes/TSP

This application is a CON of 10/424,105 04/28/2003
which is a CON of 09/939,600 08/28/2001 PAT 6,596,650
which is a CON of 09/494,036 01/31/2000 PAT 6,518,201
which is a CON of 09/380,646 09/07/1999 PAT 6,239,041
which is a 371 of PCT/JP98/00892 03/04/1998

** FOREIGN APPLICATIONS ********** Yes/TSP

JAPAN 9-50781 03/05/1997

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
** 05/06/2004

Foreign Priority claimed	STATE OR	SHEETS	TOTAL	INDEPENDENT
☑ yes ☐ no				
35 USC 119 (a-d) conditions met ☑ yes ☐ no ☐ Met after Allowance				
Verified and Acknowledged Examiner's Signature Initials	COUNTRY JAPAN	DRAWING 21	CLAIMS 6	CLAIMS 1

ADDRESS
020457
ANTONELLI, TERRY, STOUT & KRAUS, LLP
1300 NORTH SEVENTEENTH STREET
SUITE 1800
ARLINGTON , VA
22209-9889

TITLE
Method for fabricating semiconductor intergrated circuit device

☐ All Fees